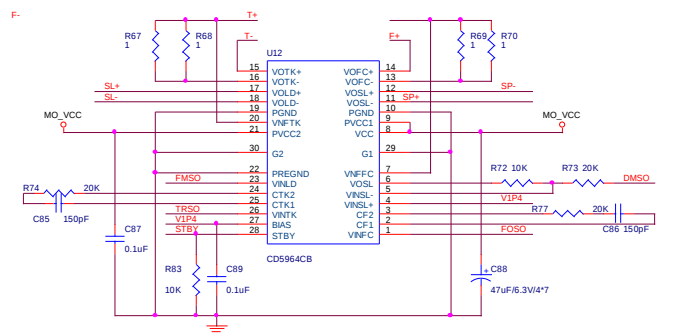
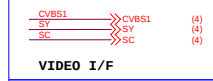
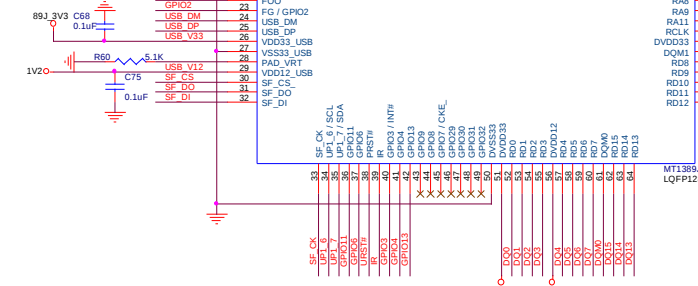
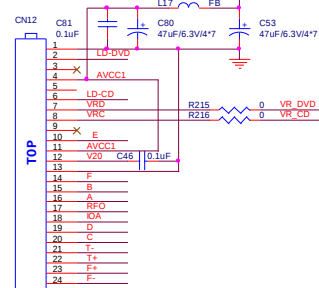
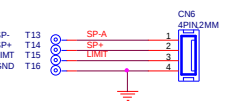
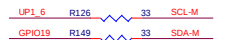
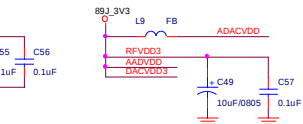
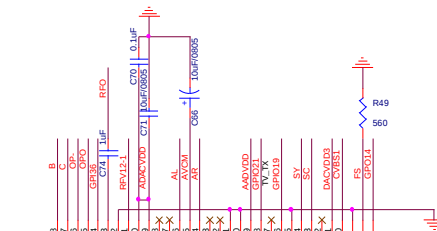
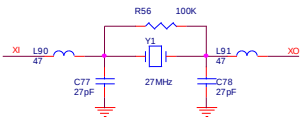
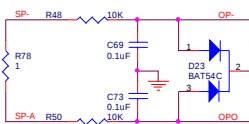
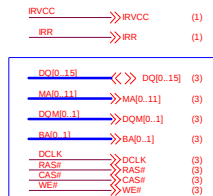
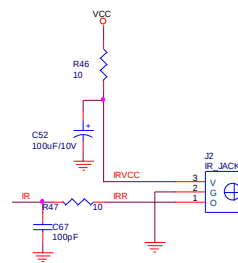
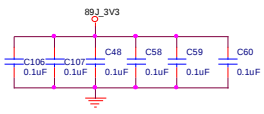
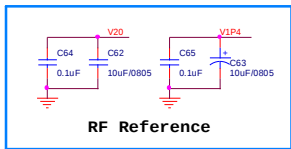
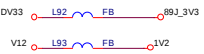
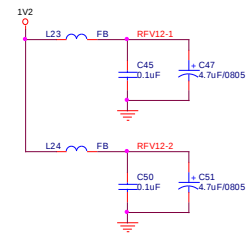
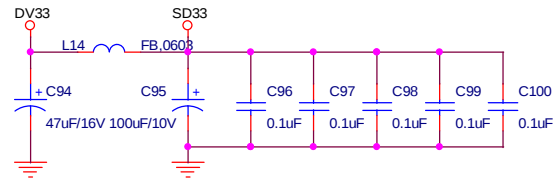
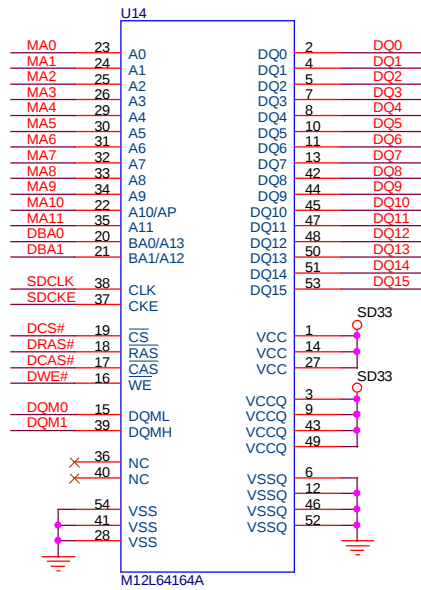
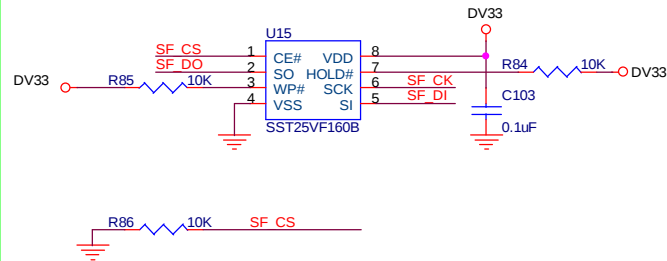




S-FLASH



DQ[0..15] <<>> DQ[0..15] (2)

MA[0..11] <<>> MA[0..11] (2)

DQM[0..1] <<>> DQM[0..1] (2)

BA[0..1] <<>> BA[0..1] (2)

DCLK <<>> DCLK (2)

RAS# <<>> RAS# (2)

CAS# <<>> CAS# (2)

WE# <<>> WE# (2)

DRAM I/F

SF CK <<>> SF CK (2)

SF CS <<>> SF CS (2)

SF DI <<>> SF DI (2)

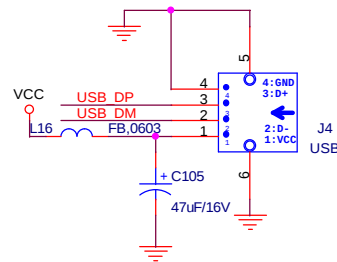
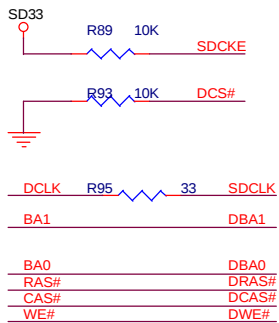
SF DO <<>> SF DO (2)

S-FLASH

USB_DP <<>> USB_DP (3)

USB_DM <<>> USB_DM (3)

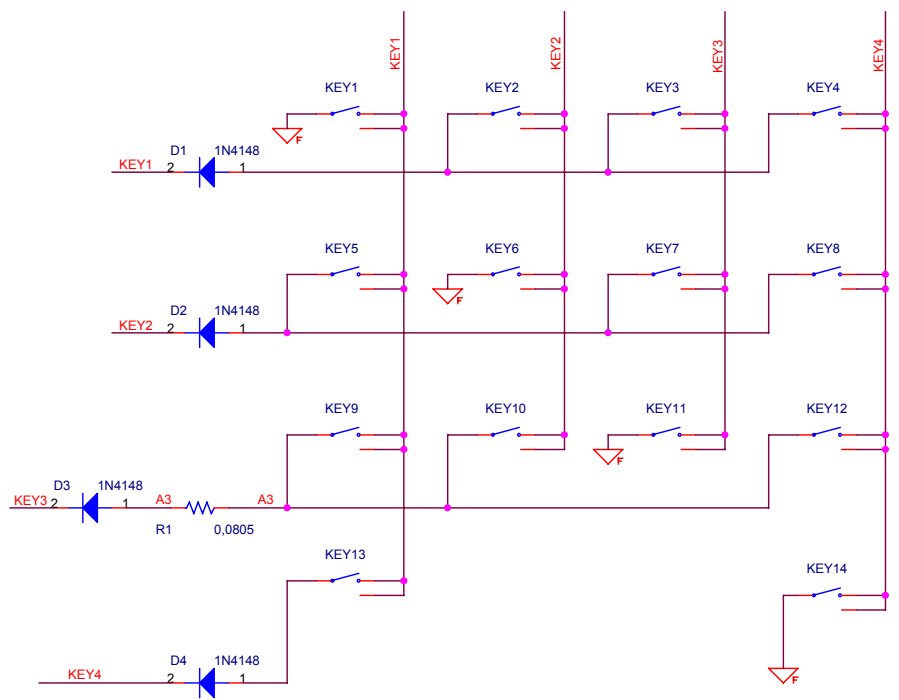
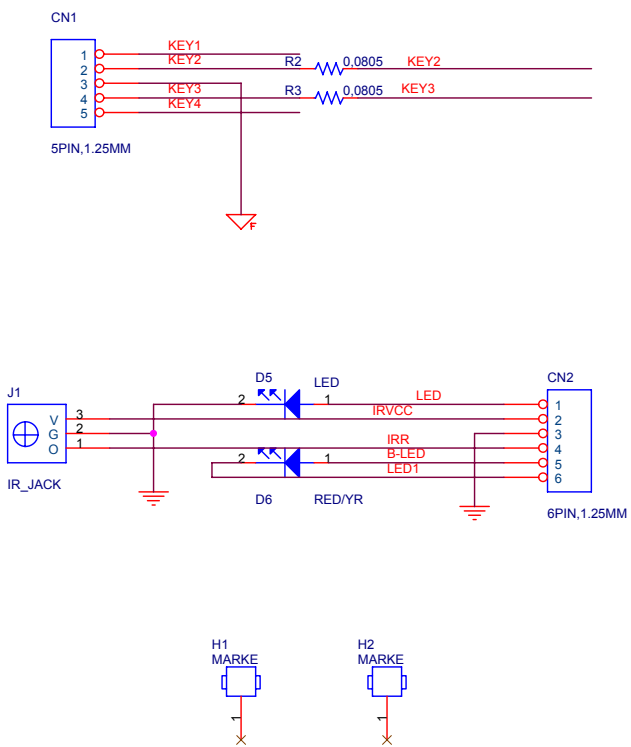
MS/SD&USB I/F



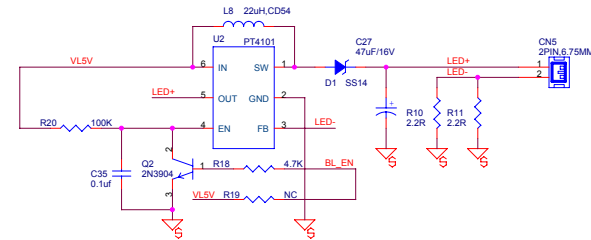
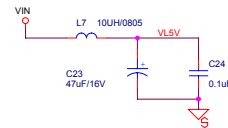
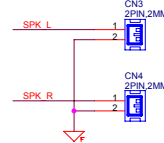
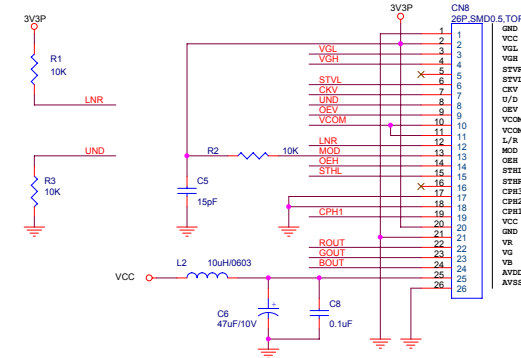
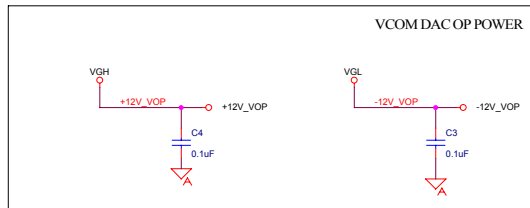
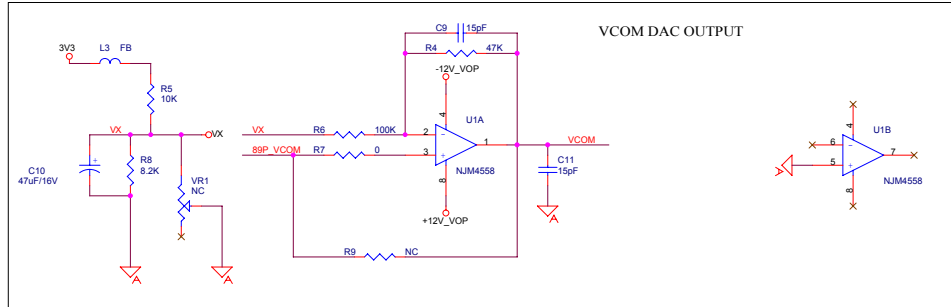
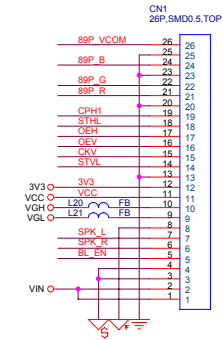
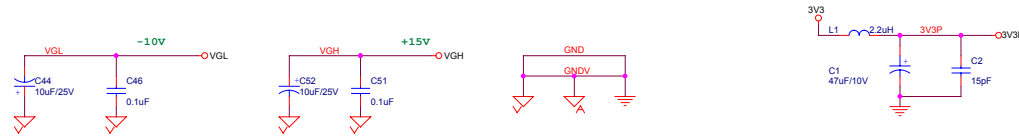
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		COMMON1389L/K HD65	
Size	Document Number	Drawn: FR_CHEN	Rev
Custom	SDRAM & FLASH	Checked: sifan_chen	1
Date:	Saturday, November 06, 2010	Sheet	3 of 5

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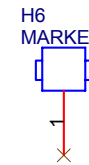
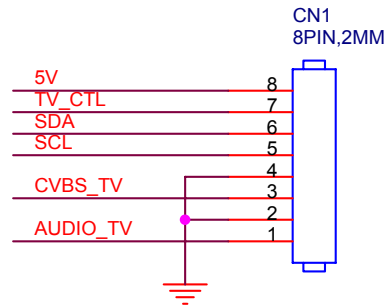
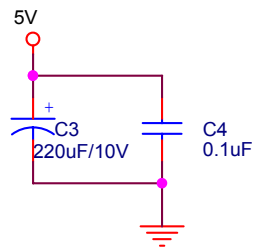
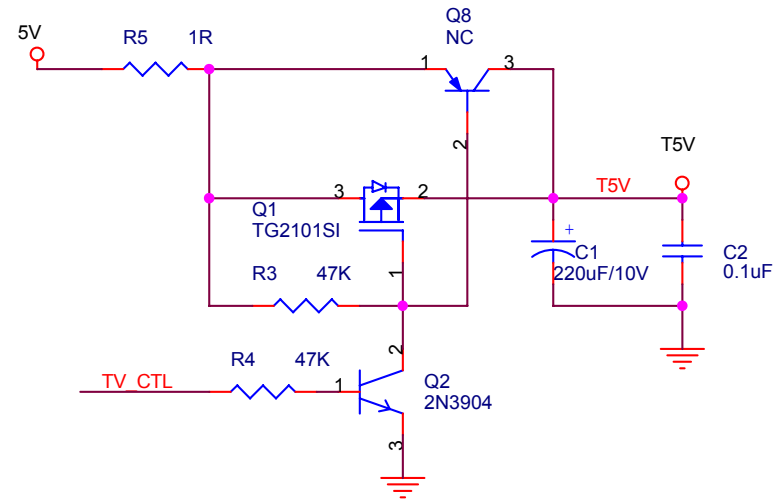
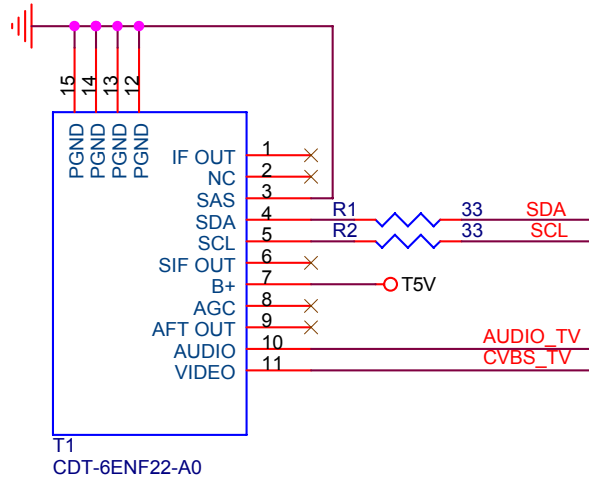
KEY BOARD



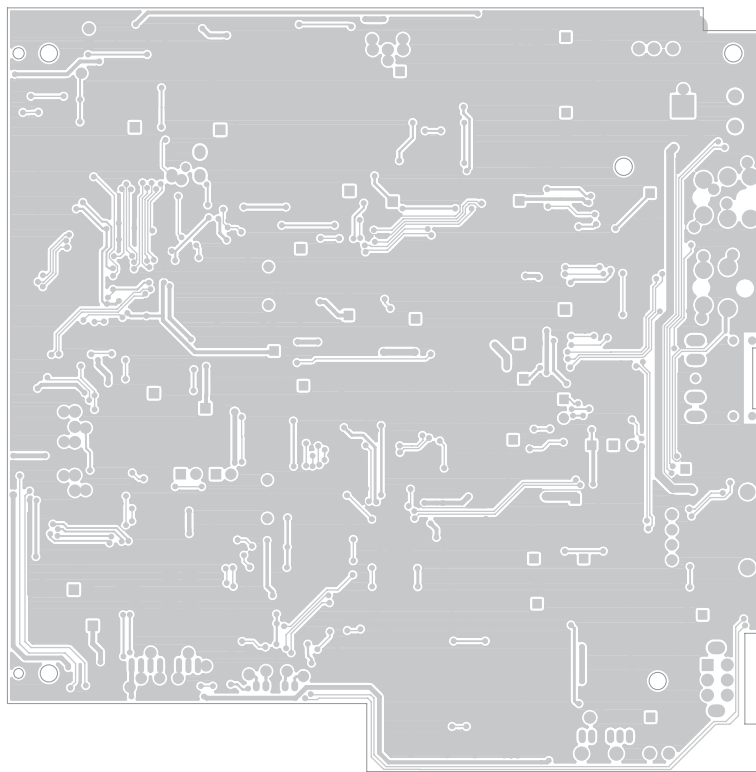
TFT BOARD



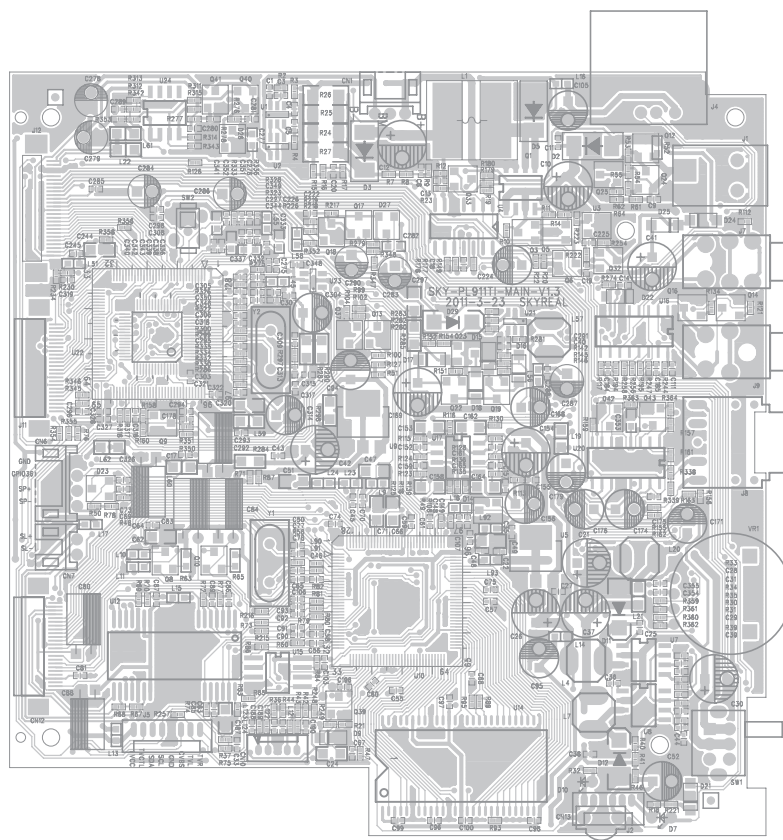
TV BOARD



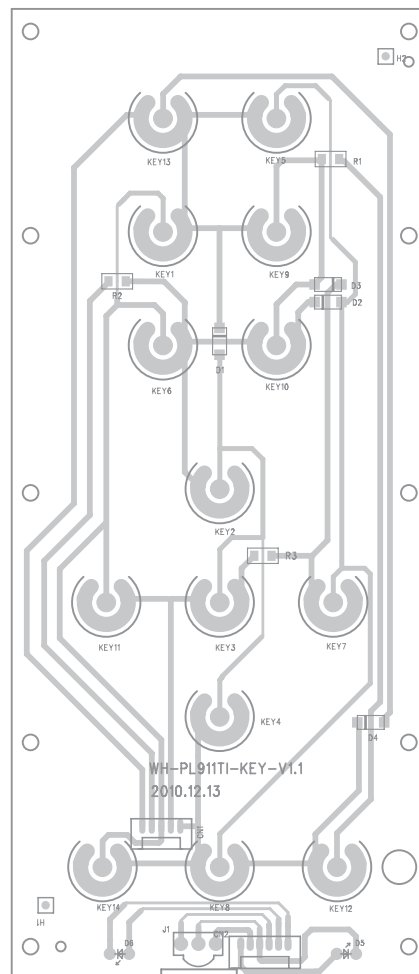
MAIN BOARD PCB



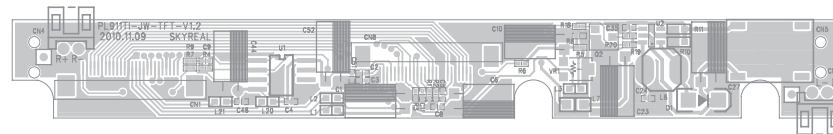
MAIN BOARD PCB-2



KEY PCB



TFT BOARD PCB



TFT BOARD PCB -2



TV BOARD PCB-1



TV BOARD PCB-2

